
5.5V-100V Vin, 1.8A Peak Current Limit, High Efficiency Asynchronous Step-down DCDC Converter

FEATURES

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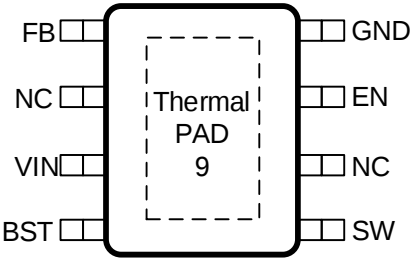
REVISION HISTORY

DEVICE ORDER INFORMATION

ABSOLUTE MAXIMUM RATINGS

- (1)
- (2)
- (3)

PIN CONFIGURATION

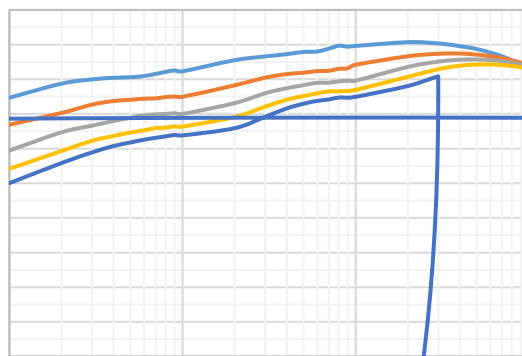
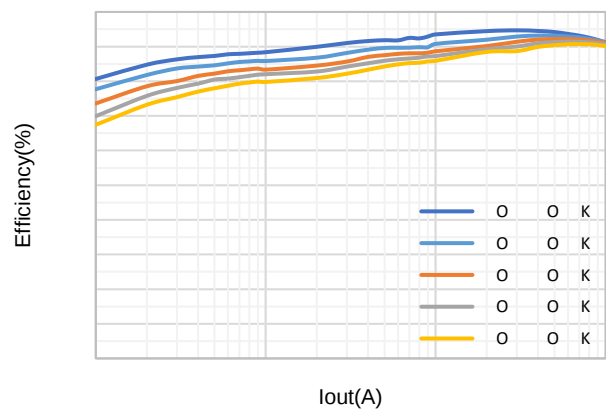


PIN FUNCTIONS

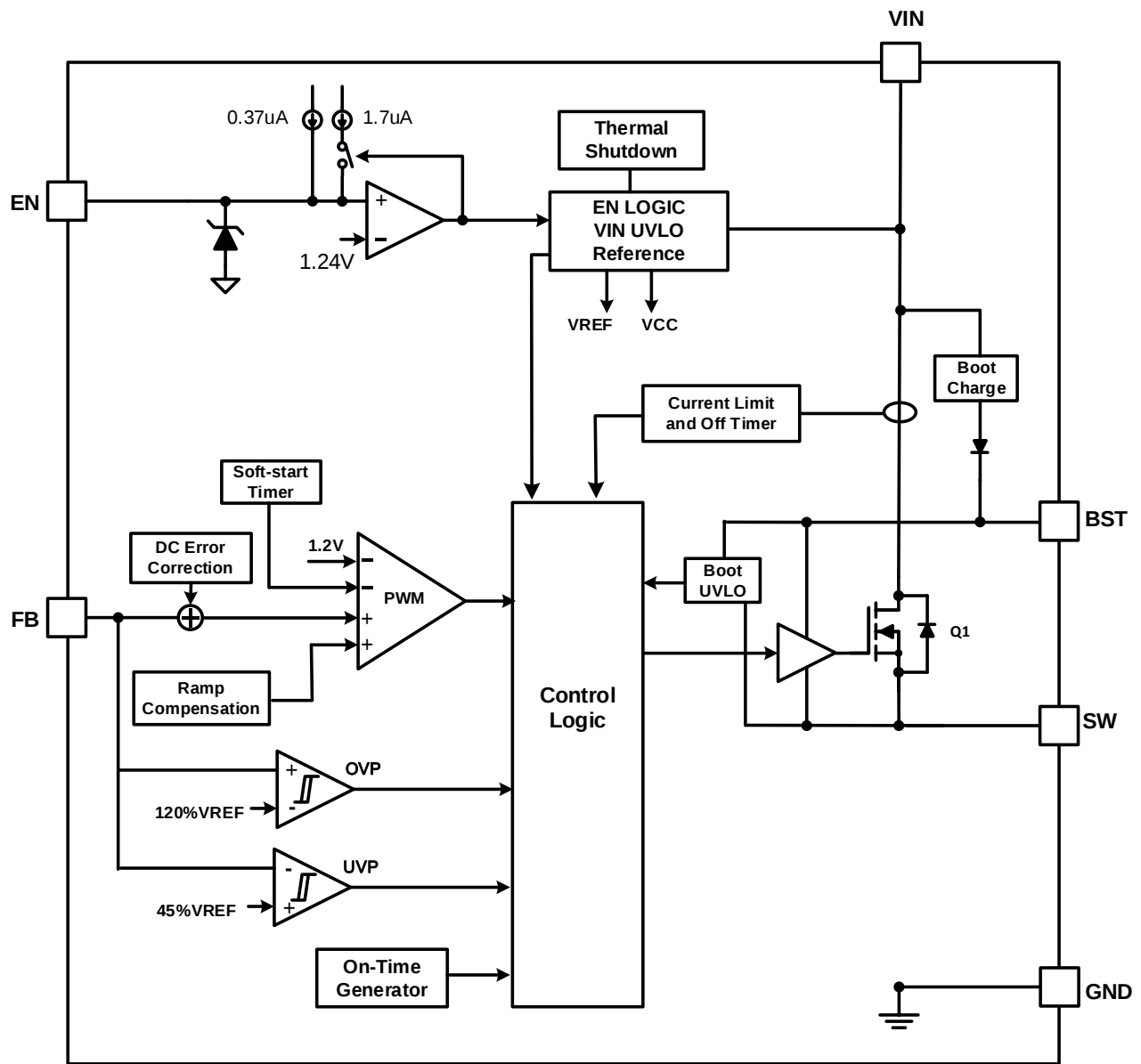
RECOMMENDED OPERATING CONDITIONS

ELECTRICAL CHARACTERISTICS

TYPICAL CHARACTERISTICS



FUNCTIONAL BLOCK DIAGRAM



OPERATION

APPLICATION INFORMATION

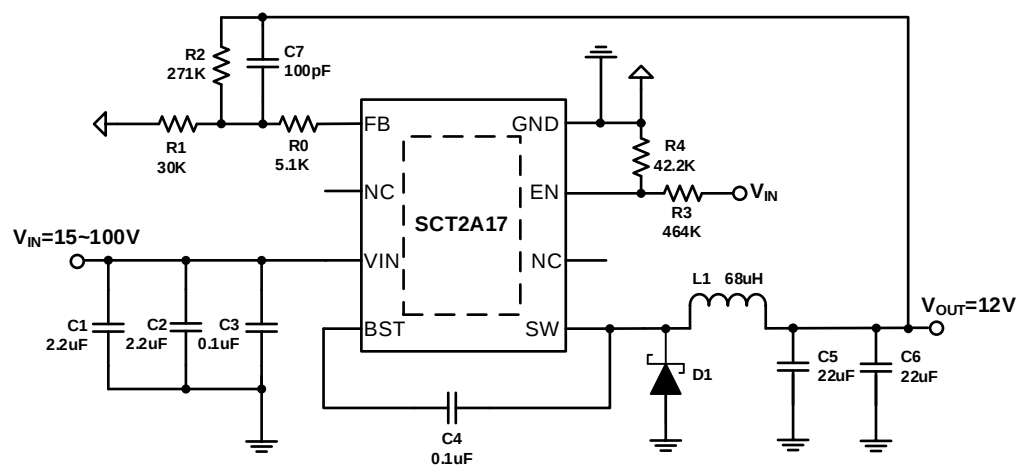


Table 1. R_1 , R_2 Value for Common Output Voltage
(Room Temperature)

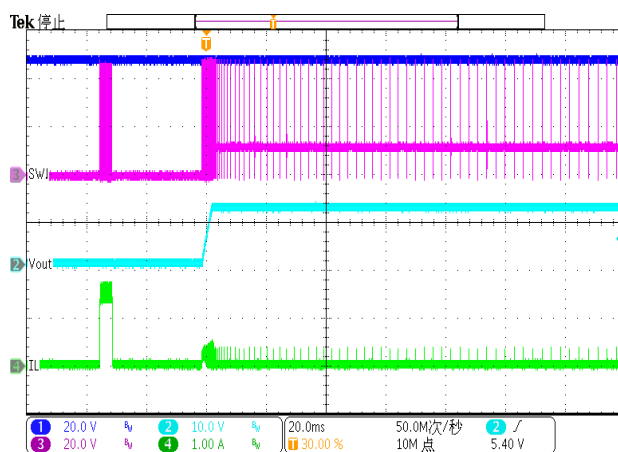
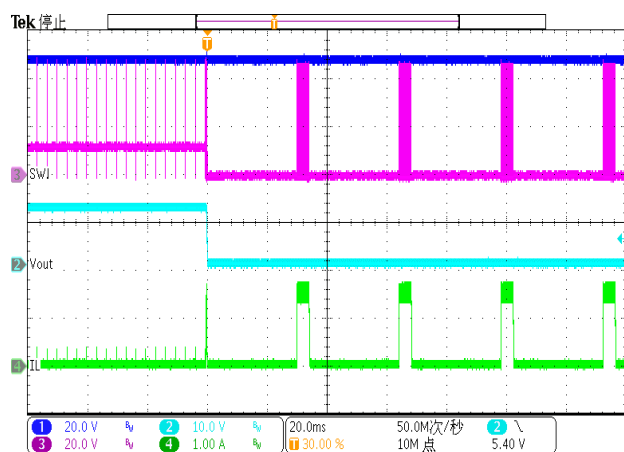
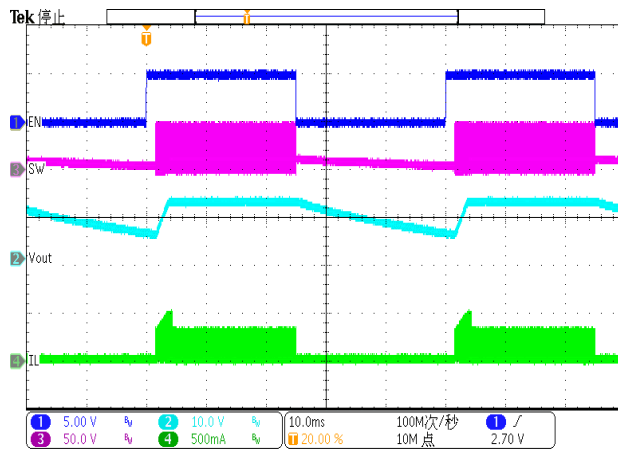
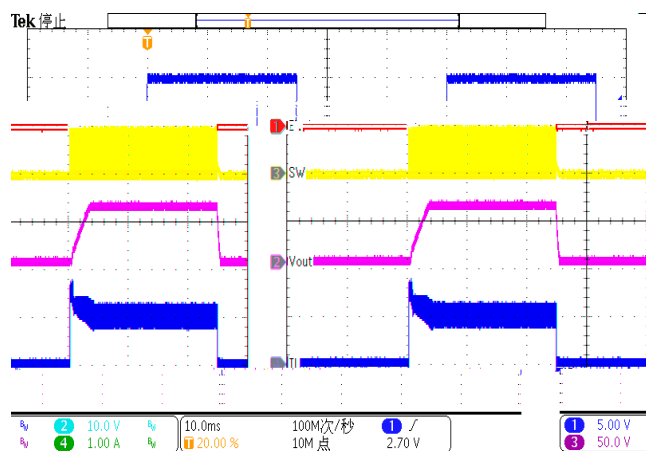
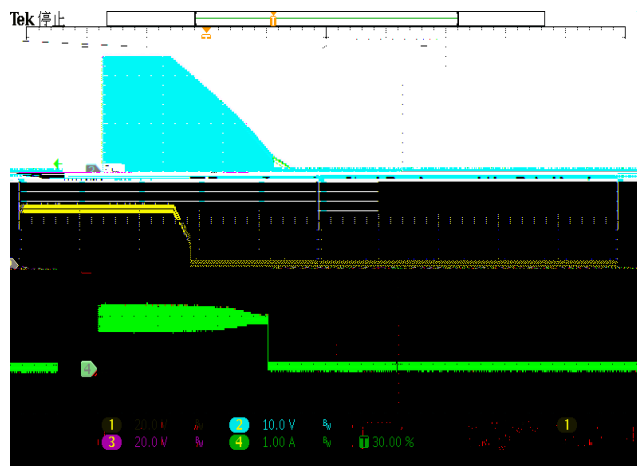
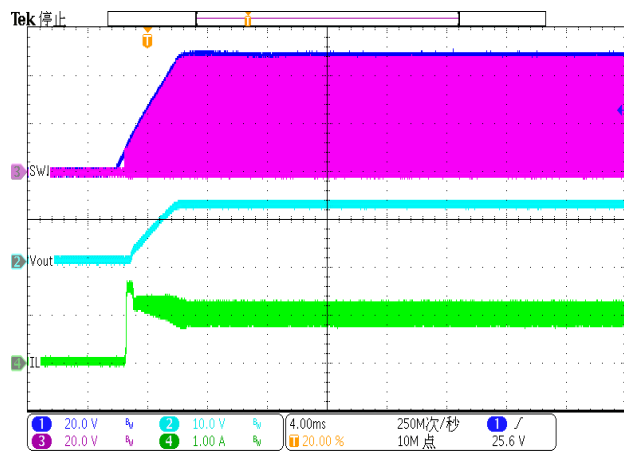
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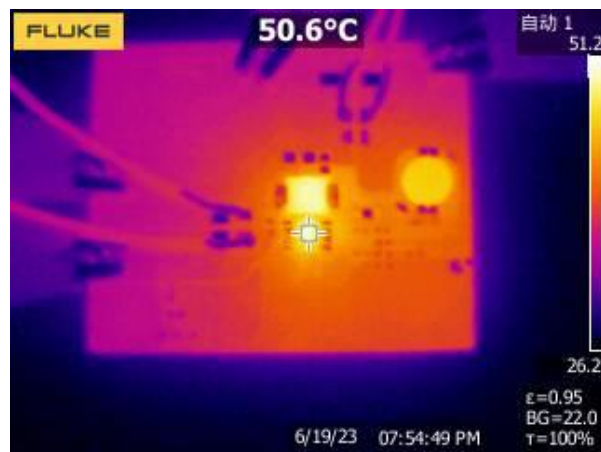
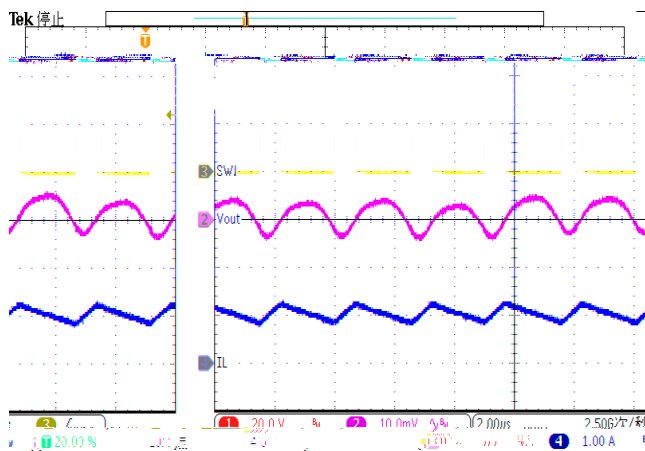
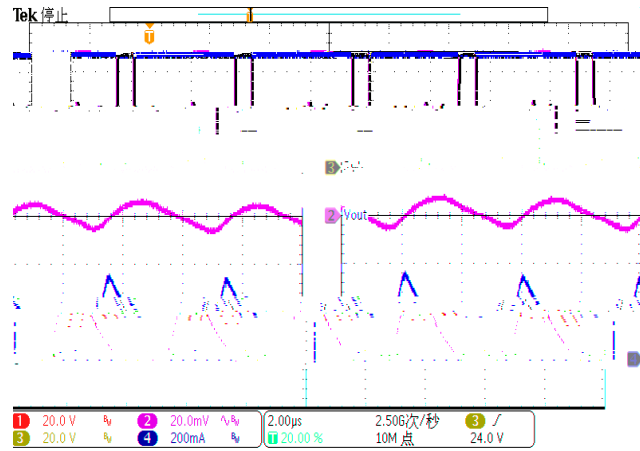
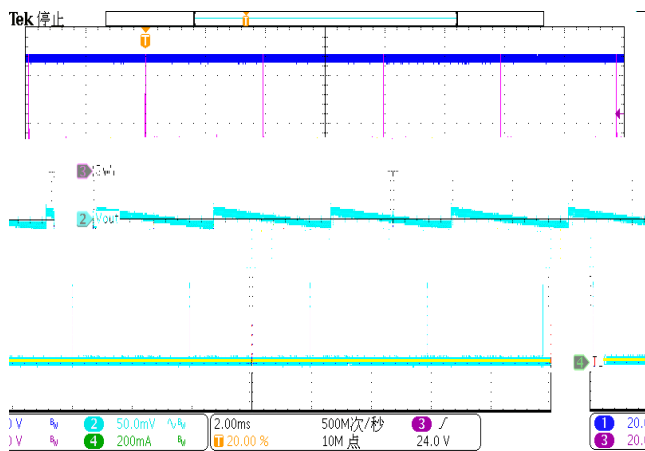
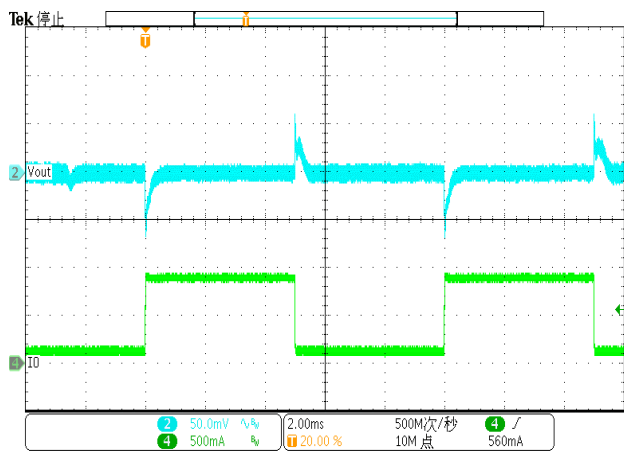
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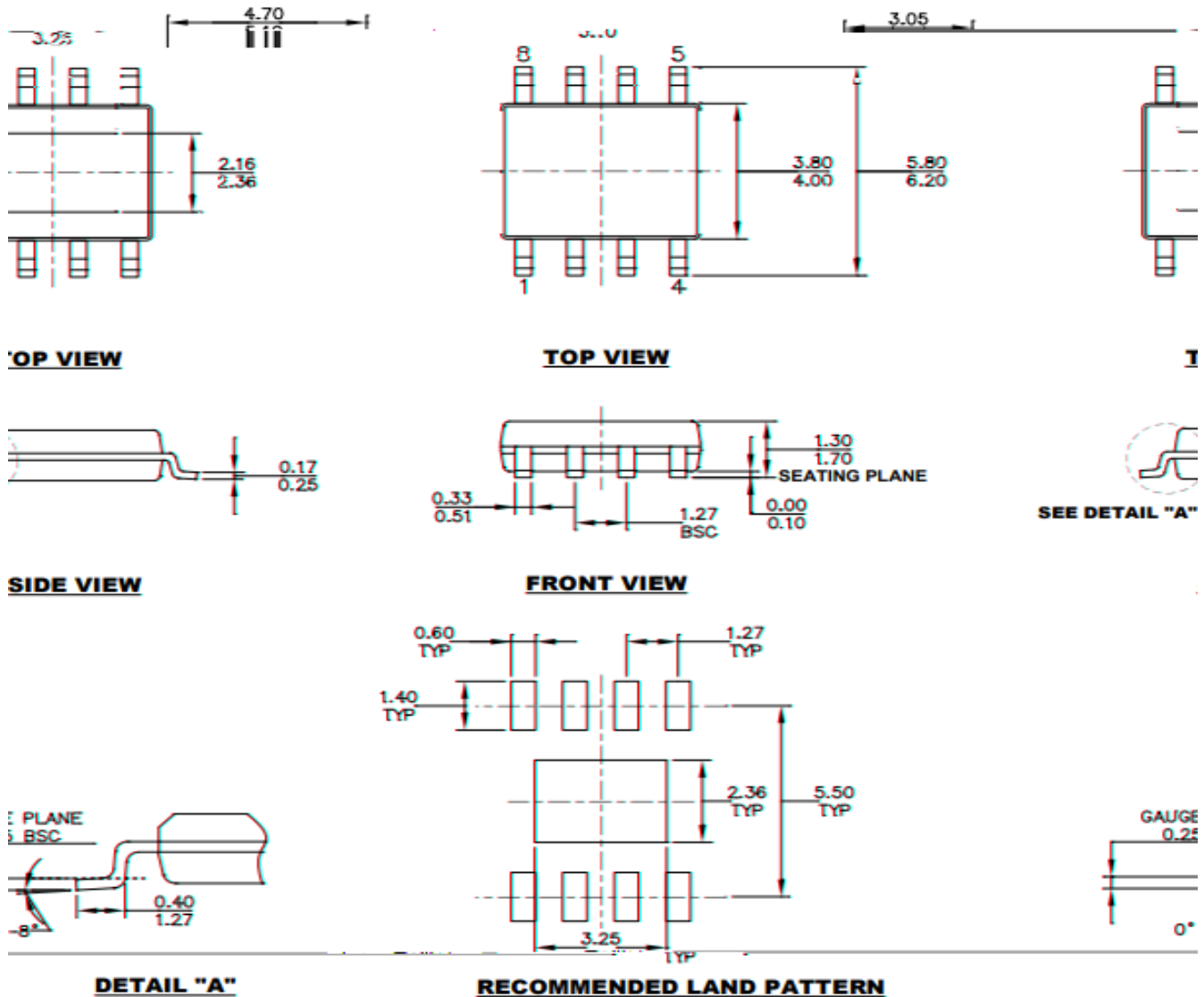
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PACKAGE INFORMATION

PACKAGE OUTLINE DRAWING FOR 8L SOP-EP POD-0022 Revision 0.0



NOTE:

1) IS IN MILLIMETERS.
2) USIONS OR GATE BURRS.
3) PROTRUSIONS.
4) SHALL BE 0.004" INCHES MAX.

- 1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET
- 2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTR
- 3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR
- 4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) S
- 5) DRAWING REFERENCE TO JEDEC MS-012, VARIATION AA.
- 6) DRAWING IS NOT TO SCALE.

