

N A ? K

? LM ?K

-
-

SCT1271

NAKAGF A L G

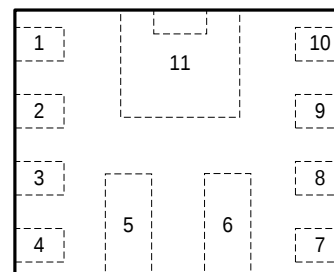
NA G A F G E L A G F

ORDERABLE DEVICE	PACKAGING TYPE	STANDARD PACK QTY	PACKAGE MARKING	PINS	PACKAGE DESCRIPTION

KGDML E P A E M E L A F K

H A F G F A M L A G F

DESCRIPTION	MIN	MAX	UNIT
			o
			o
			o



H A F M F L A G F K

NAME	NO.	PIN FUNCTION

2 GE E 2F 2 GH 2 LAF GF AAGFK

PARAMETER	DEFINITION	MIN	MAX	UNIT

2K LAF K

PARAMETER	DEFINITION	MIN	MAX	UNIT

L 2 E DAF G E LAGF

PARAMETER	THERMAL METRIC	QFN-11L	UNIT

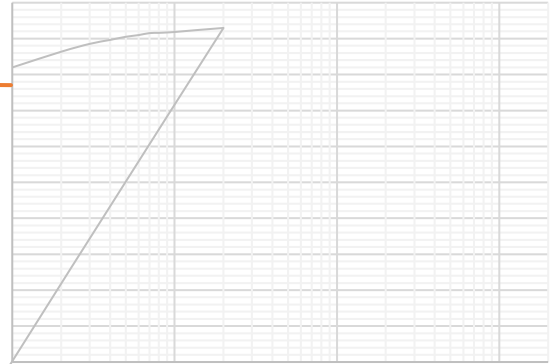
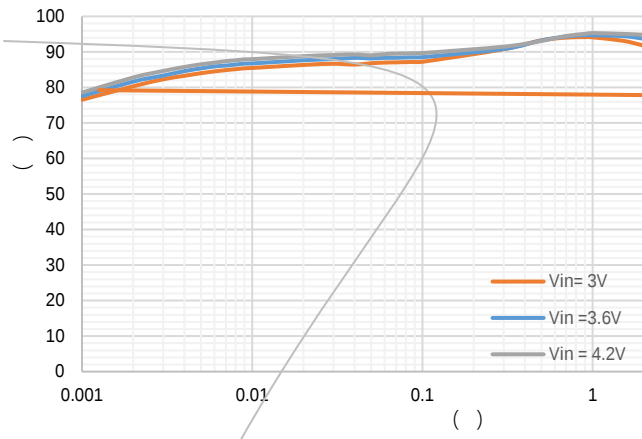
SCT1271

□D□ L A D

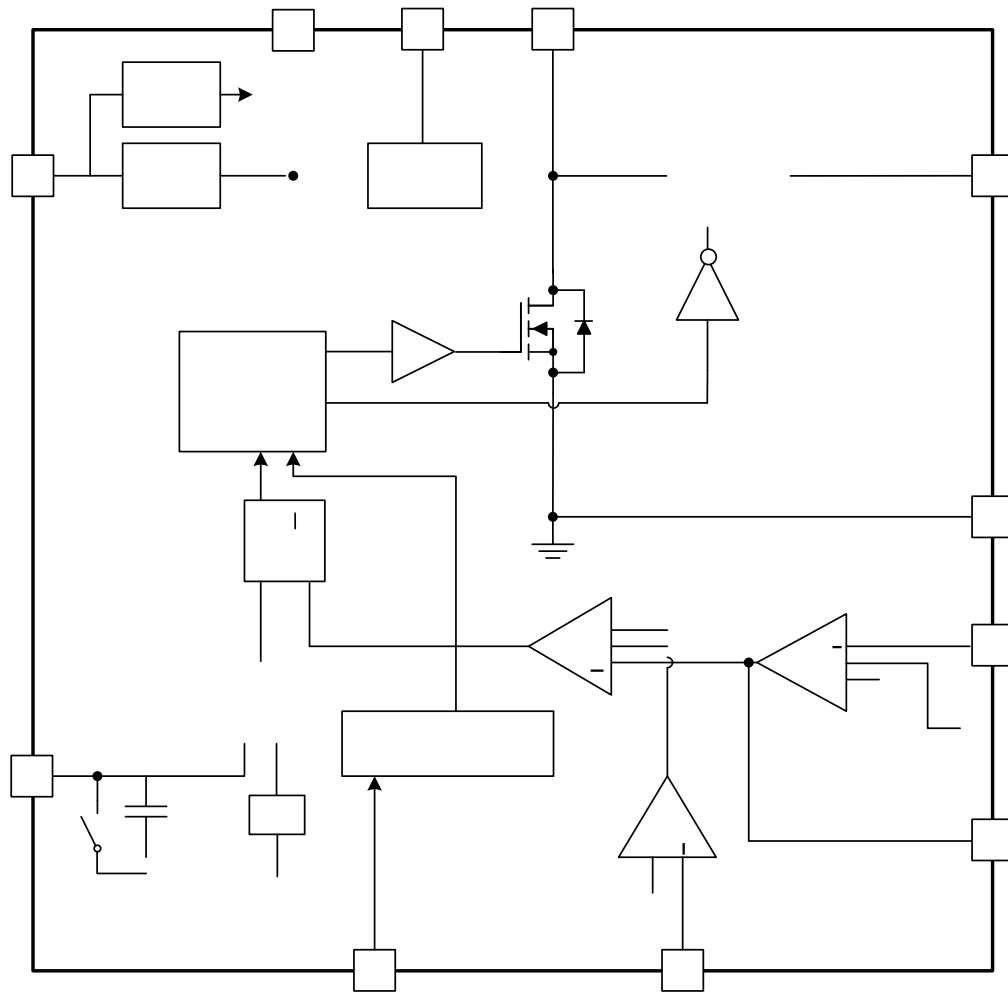
L ? A L A K

SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Power Supply and Output						
Reference and Control Loop						
Power MOSFETs						
Current Limit						
Enable						
Switching Frequency						
Protection						

L HA D L? ALAK



MF LAGF D DG C A E



GH? LAGF

Overview

Adjustable Peak Current Limit

-
-

Adjustable Switching Frequency

-
-
-
-
-

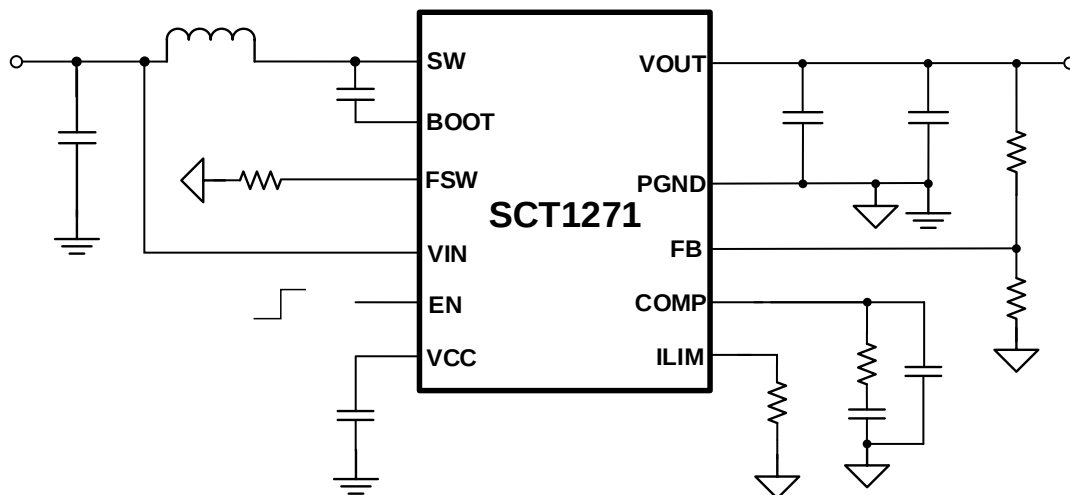
Over Voltage Protection and Minimum On-time

Thermal Shutdown

SCT1271

HHDA LAGF AF G E LAGF

Typical Application



Design Parameters

Design Parameters	Example Value

Switching Frequency

-
-
-
-
-

F _{SW}	R _{FREQ}

Peak Current Limit

-
-

I _{LIM}	R _{LIM}

Output Voltage

-

V _{OUT}	R ₁	R ₂

Inductor Selection

Table 4. Recommended Inductors

Part Number	L (uH)	DCR Max (mΩ)	Saturation Current/Heat Rating Current (A)	Size Max (LxWxH mm)	Vendor

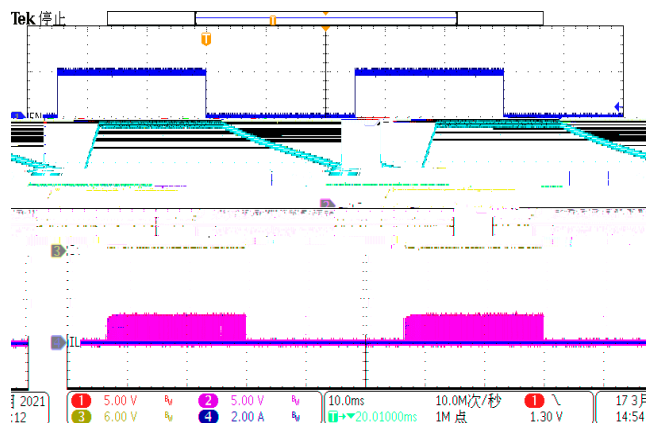
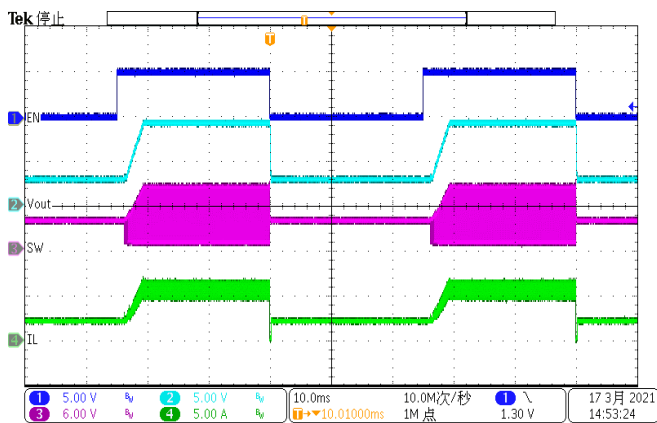
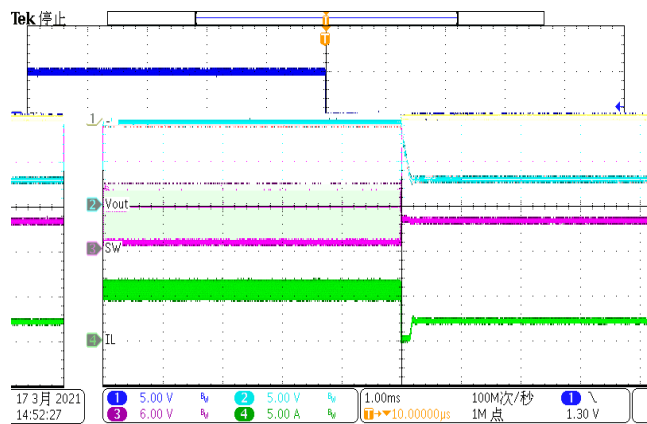
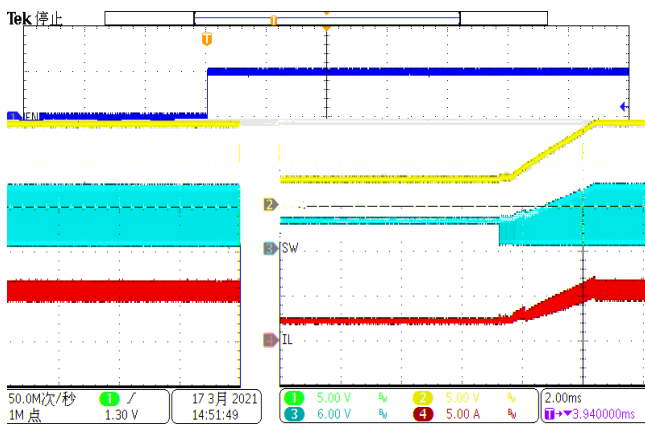
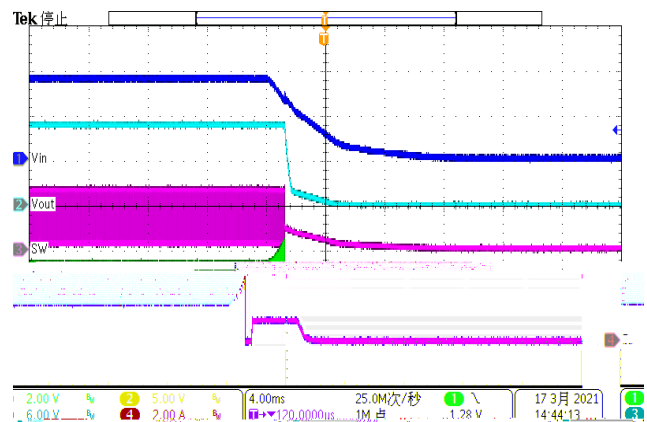
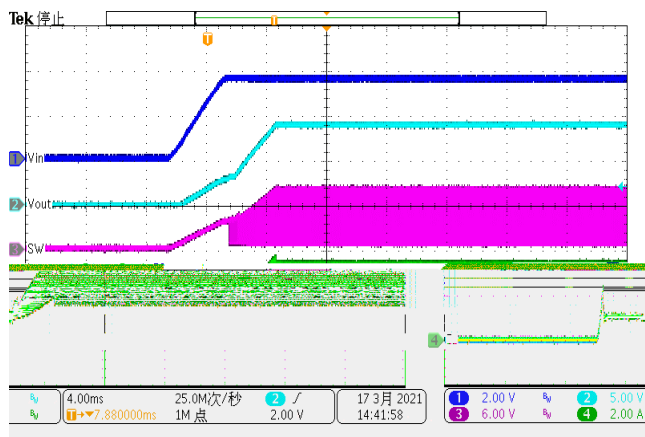
Input Capacitor Selection

Output Capacitor Selection

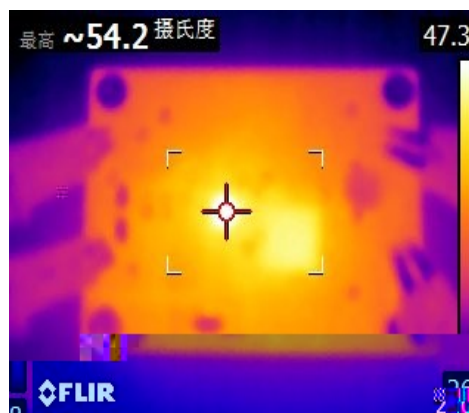
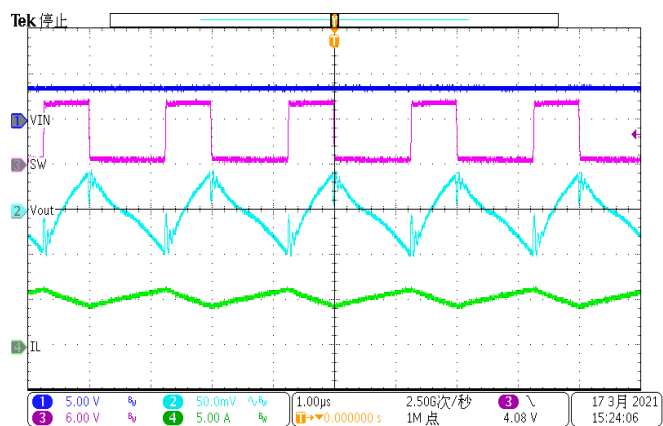
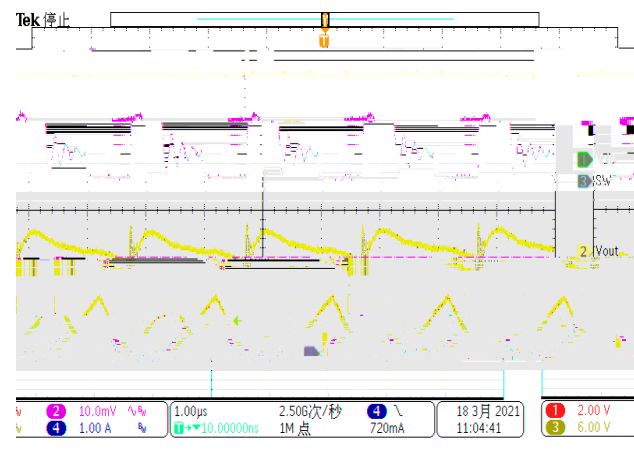
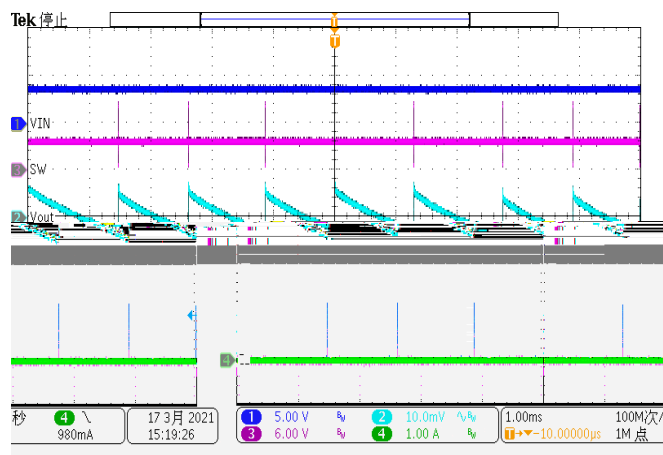
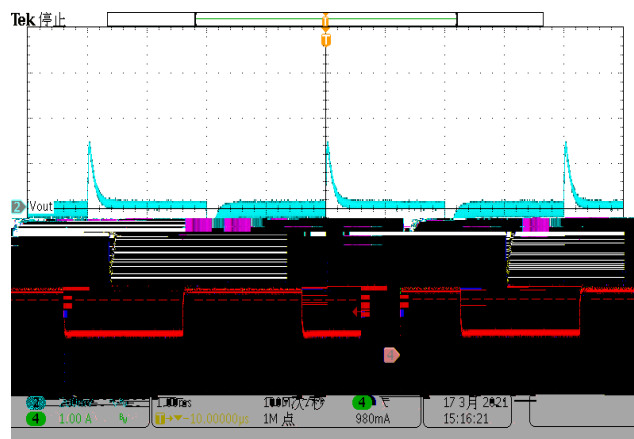
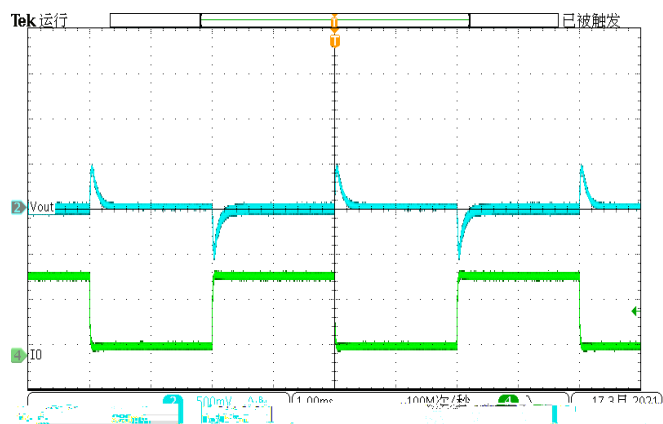
-
-
-
-
-
-
-
-

Loop Stability

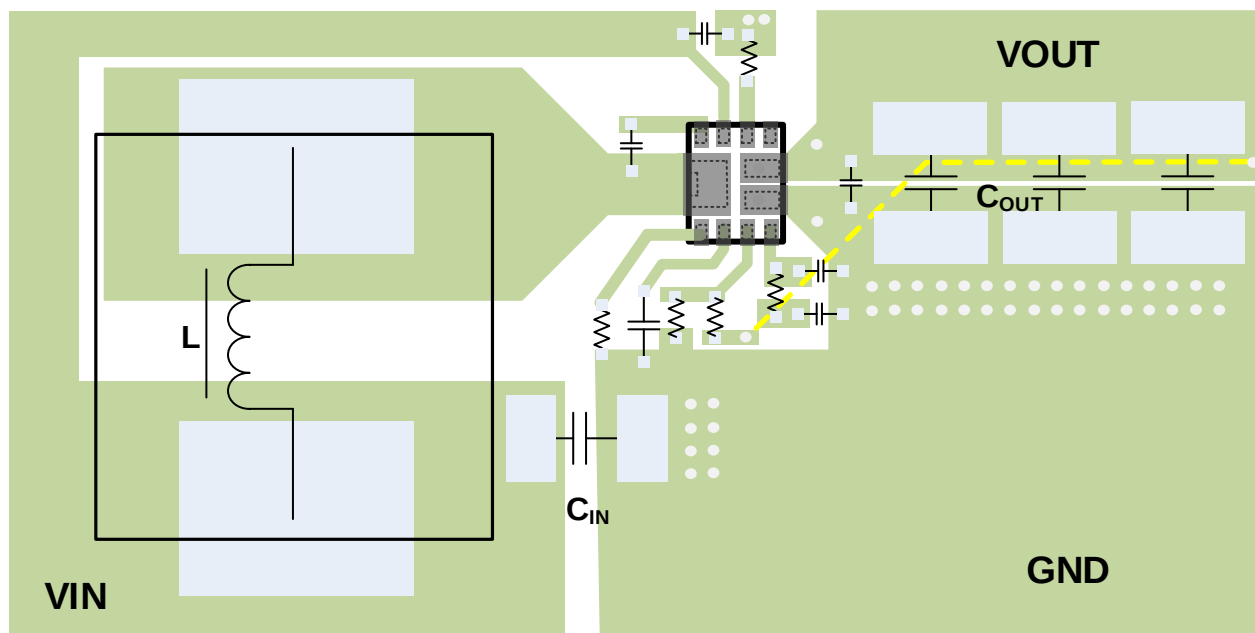
Application Waveforms



Application Waveforms(continued)



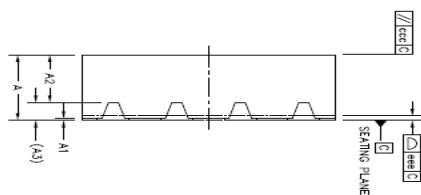
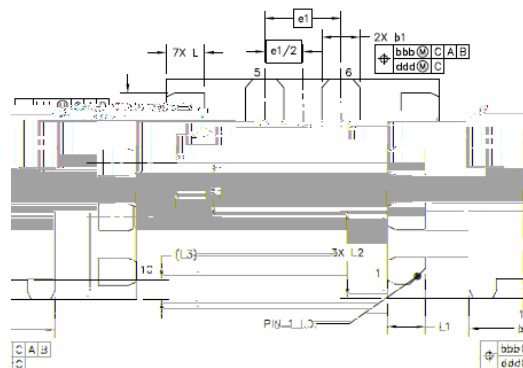
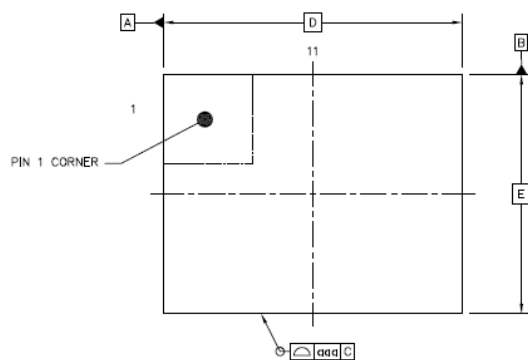
Layout Guideline



Thermal Considerations

-
-

H C ? AF G E LAGF



NOTE:

[illegible]

L H2 F 22DAF G E LAGF

