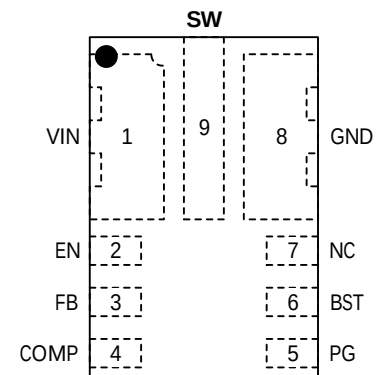


Efficiency vs Output Current

Over operating free-air temperature unless otherwise noted⁽¹⁾

DESCRIPTION	MIN	MAX	UNIT
BST	-0.3	48	V
VIN, SW, EN	-0.3	40	V
FB, COMP	-0.3	6	V
BST-SW	-0.3	6	V
Operating junction temperature ⁽²⁾	-40	125	C
Junction temperature	-40	150	C
Storage temperature T _{STG}	-65	150	C

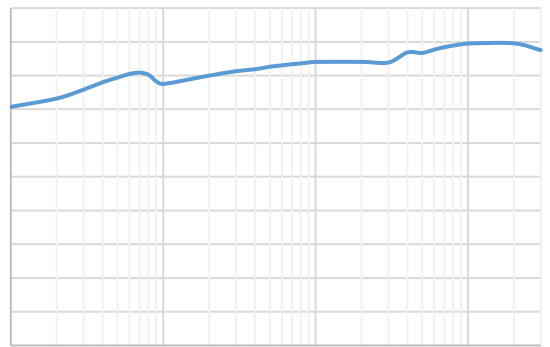
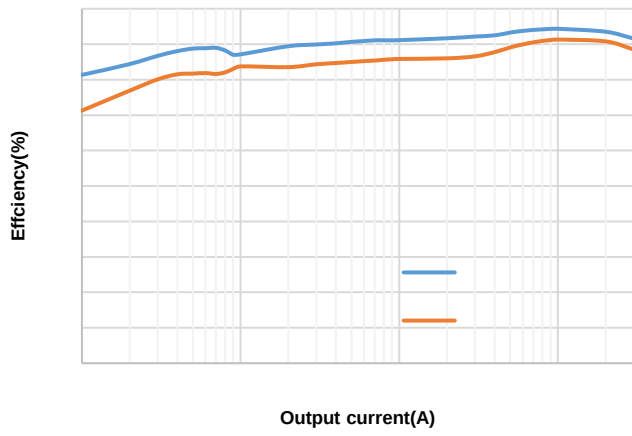


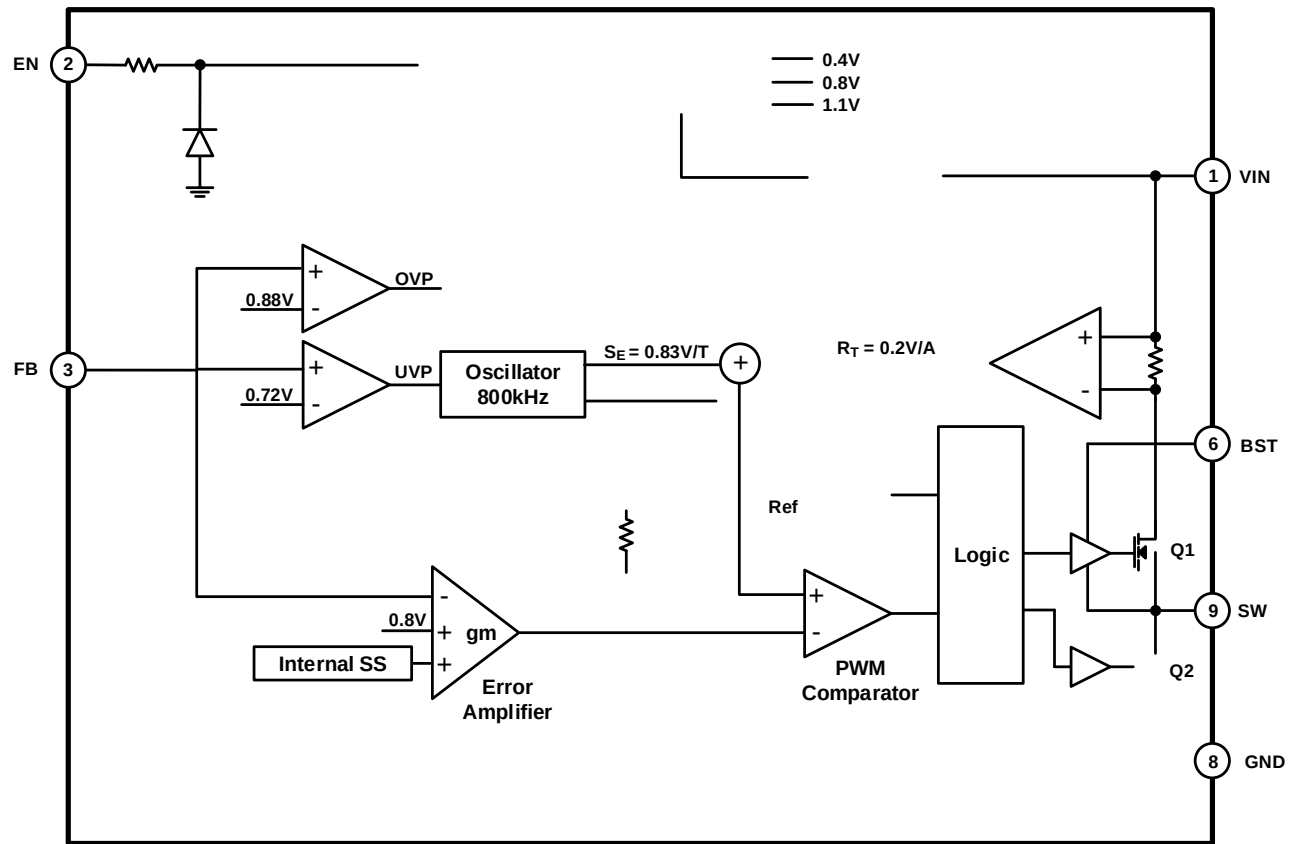
(1)

(2)

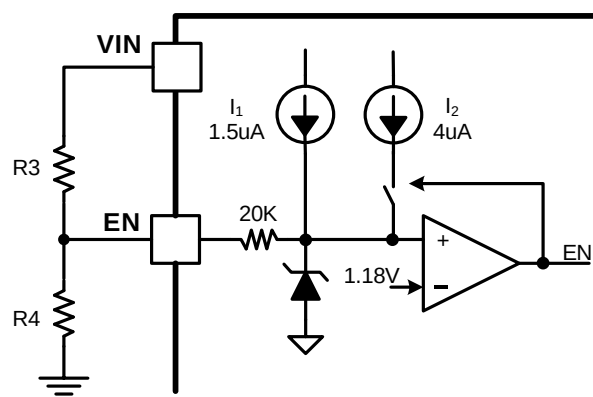
VIN	1	Power supply input. Must be locally bypassed.
EN	2	Enable logic input. Floating the pin enables the device. This pin supports high voltage input up to VIN supply to be connected VIN directly to enable the device automatically. The device has precision enable thresholds 1.18V rising / 1.1V falling for programmable UVLO threshold and hysteresis.
FB	3	Buck converter output feedback sensing voltage. Connect a resistor divider from VOUT to FB to set up output voltage. The device regulates FB to the internal reference of 0.8V typical.
COMP	4	Compensation pin. Connect this pin to GND with a 1KΩ resistor for internal compensation. Add an RC network to adjust the loop response externally. See Loop Response Design for more details.

PG	5	Open drain power-good output that is pulled to GND when the output voltage is out of its regulation limits or during soft-start. There is an internal 5M Ω pull-up resistor.
BST	6	Power supply for the high-side power MOSFET gate driver. Must connect a 0.1 μ F or greater ceramic capacitor between BST pin and SW node.
NC	7	NC
GND	8	Power ground. Must be soldered directly to ground plane.
SW	9	Switching node of the buck converter.



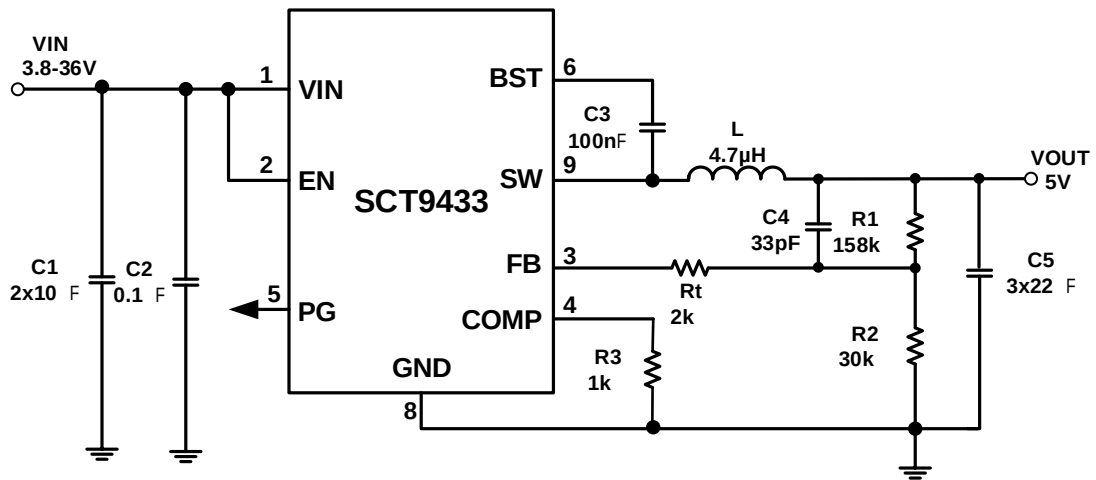


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(1)

(2)



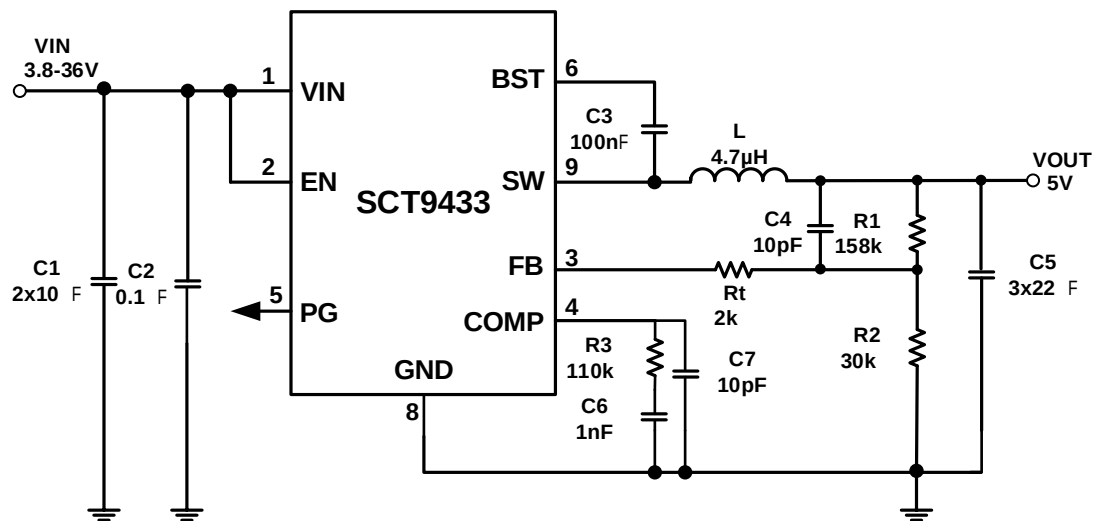
Design Parameters	Example Value
Input Voltage	12V
Output Voltage	5V
Output Current	3A
Output voltage ripple (peak to peak)	$\pm 0.03V$
Switching Frequency	800kHz

(3)

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_____ (10)

_____ (11)



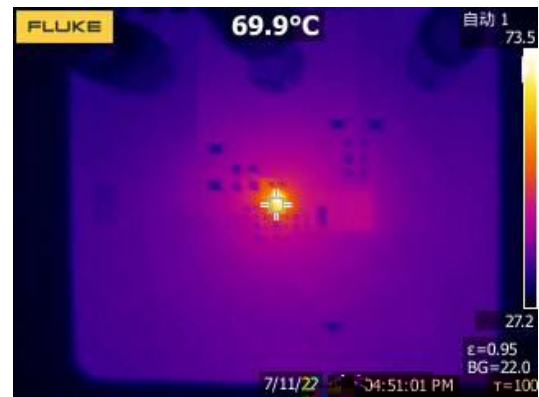
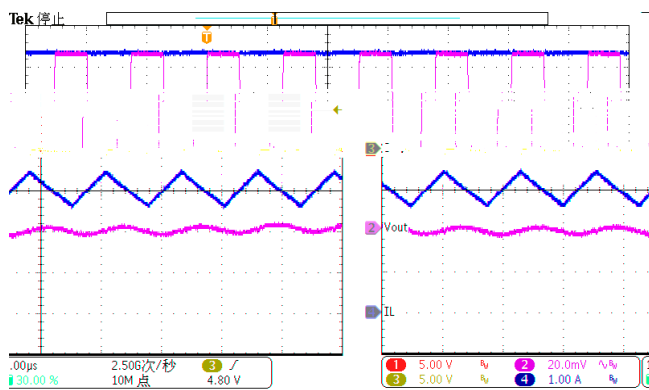
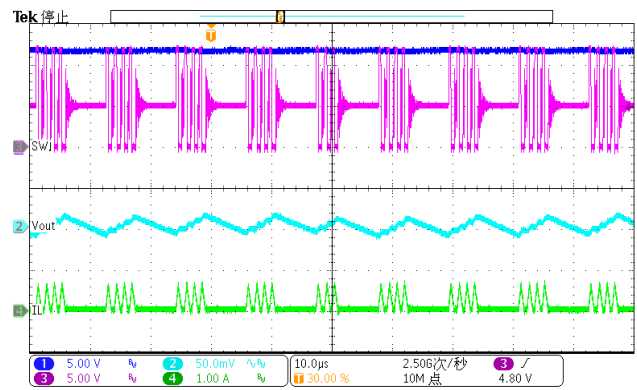
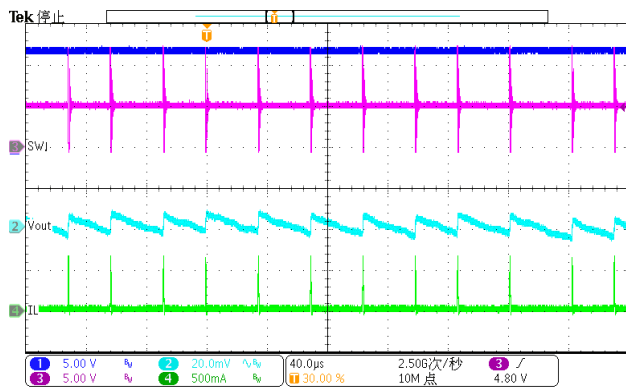
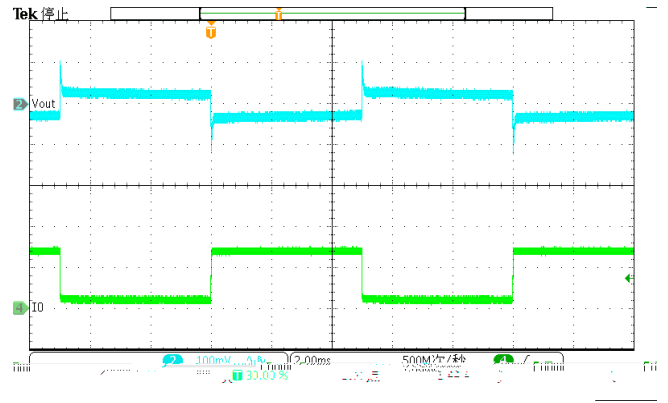
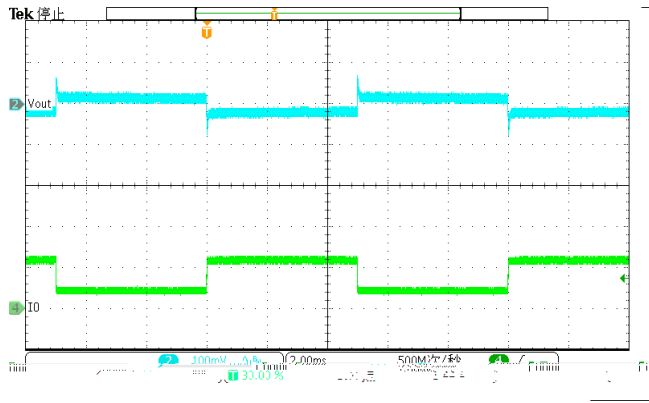
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