
3.6V-36V Vin, 3A, High Efficiency Synchronous Step-down DCDC Converter

FEATURES

DESCRIPTION

APPLICATIONS

SCT2434DQ

REVISION HISTORY

DEVICE ORDER INFORMATION

ORDERABLE DEVICE	PACKAGING TYPE	STANDARD PACK QTY	PACKAGE MARKING	PINS	PACKAGE DESCRIPTION	MSL

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	MIN	MAX	UNIT

PIN CONFIGURATION

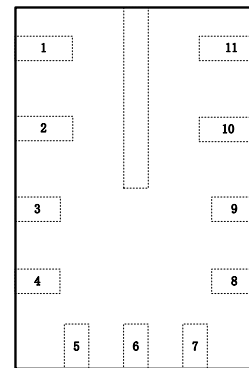


Figure 1. 12-Lead QFN 2mm×3mm

PIN FUNCTIONS

NAME	NO.	PIN FUNCTION

RECOMMENDED OPERATING CONDITIONS

PARAMETER	DEFINITION	MIN	MAX	UNIT

ESD RATINGS

PARAMETER	DEFINITION	MIN	MAX	UNIT

THERMAL INFORMATION

PARAMETER	THERMAL METRIC	QFN-12L
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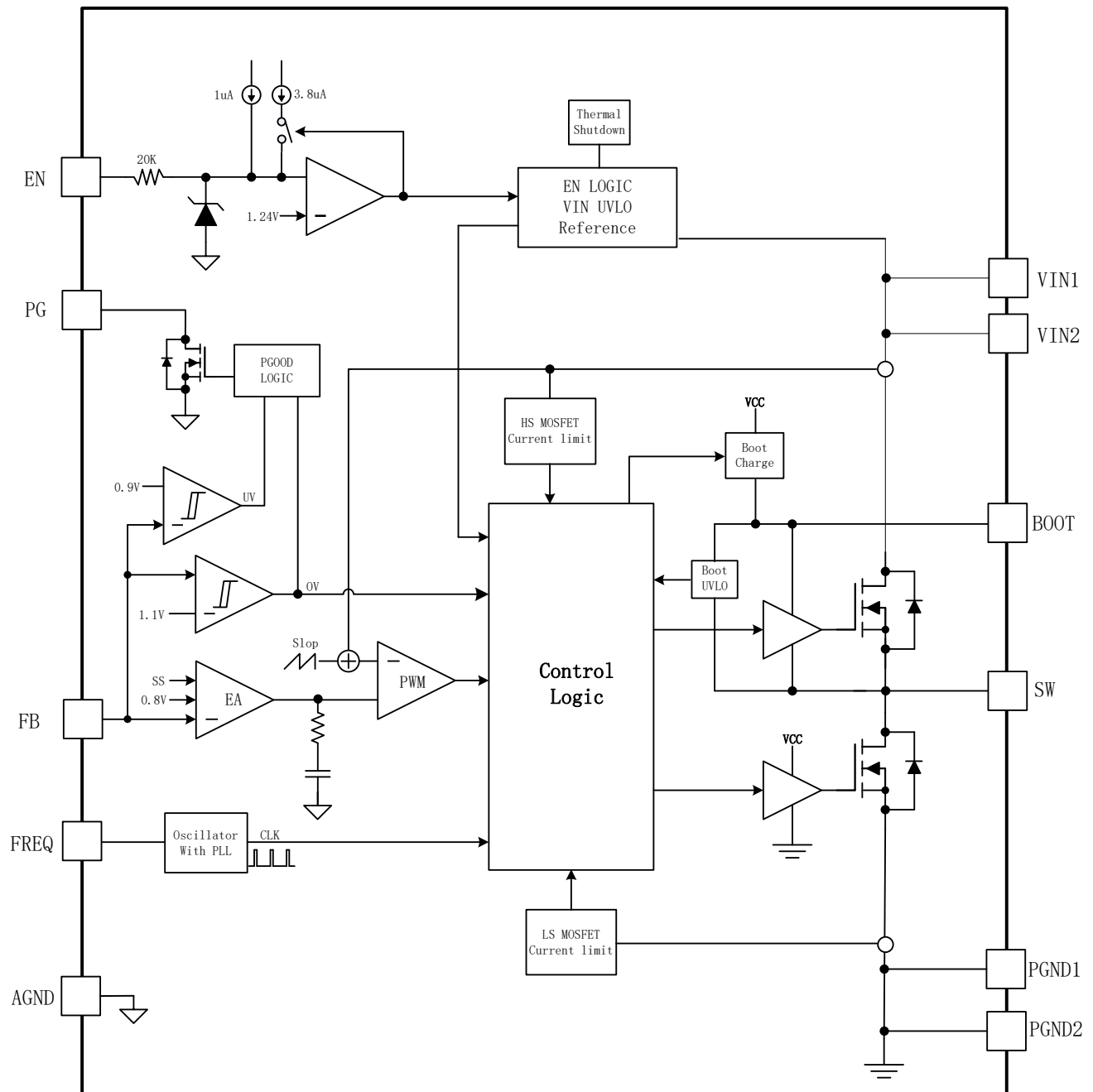
ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Power Supply						
Power MOSFETs						
Reference						
Current Limit and Over Current Protection						
Enable and Soft Startup						
Switching Frequency and External Clock Synchronization						
Power Good						

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SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Protection						

FUNCTIONAL BLOCK DIAGRAM



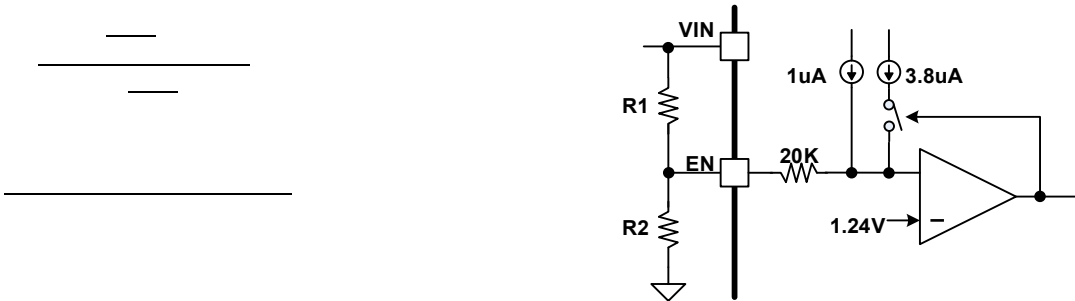
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OPERATION

Overview

Peak Current Mode Control

Enable and Under Voltage Lockout Threshold



Output Voltage

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Internal Soft-Start

Frequency Spread Spectrum

Fsw	R ₆ (R _{Fsw})

Fsw	R ₆ (R _{Fsw})

Bootstrap Voltage Regulator and Low Drop-out Operation

Over Current Limit and Hiccup Mode

Thermal Shutdown

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Output Voltage

V _{OUT}	R ₃	R ₄

Switching Frequency

F _{sw}	R ₆ (R _{Fsw})

F _{sw}	R ₆ (R _{Fsw})

Under Voltage Lock-Out

Inductor Selection

Input Capacitor Selection

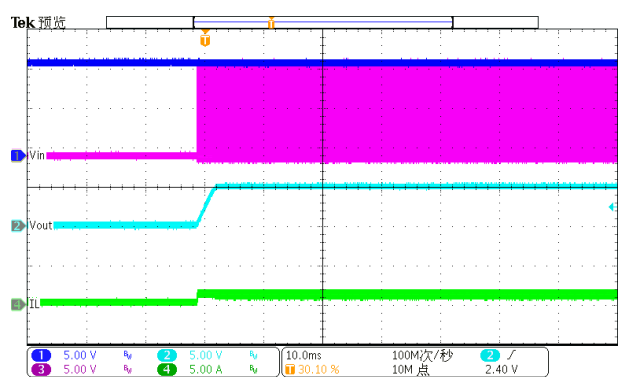
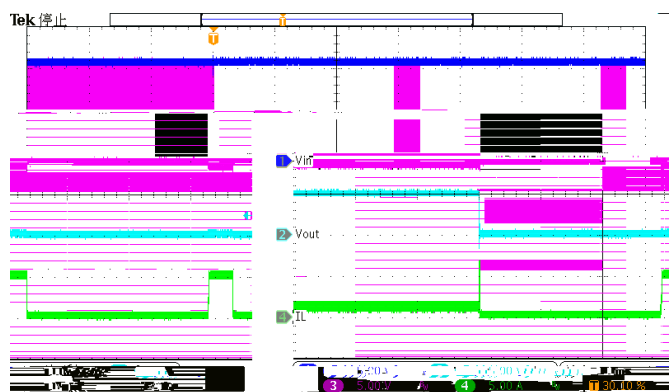
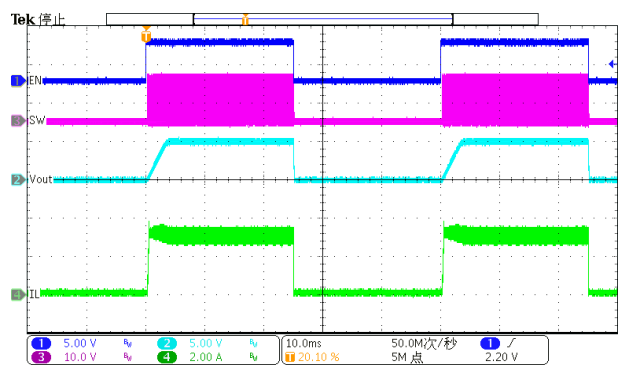
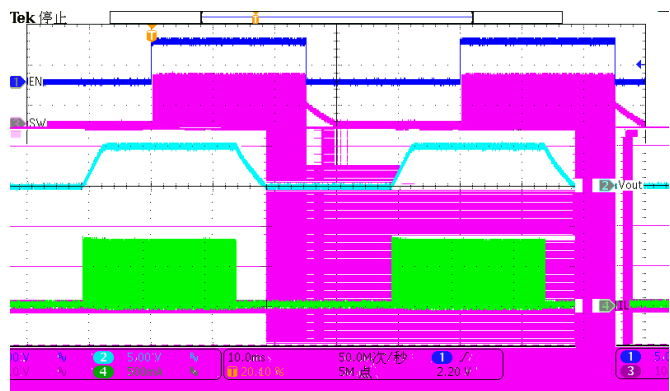
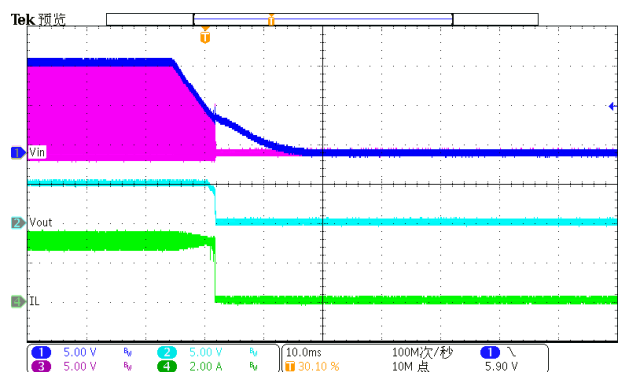
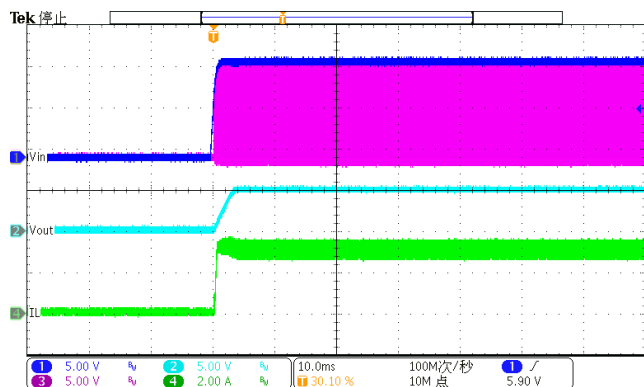
Bootstrap Capacitor Selection

Output Capacitor Selection

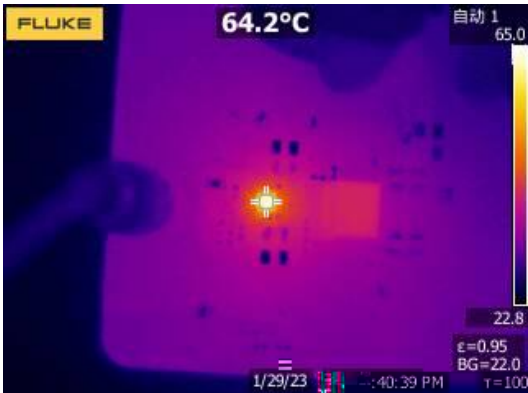
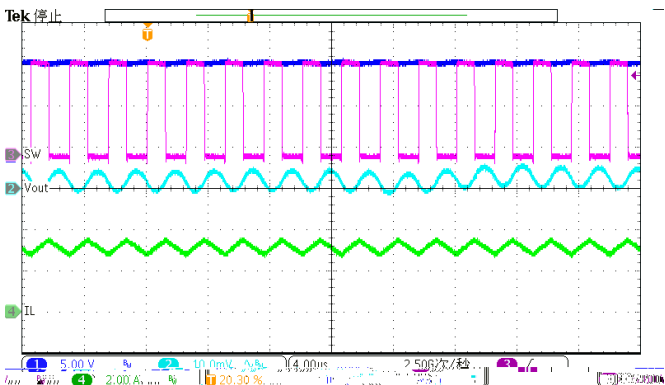
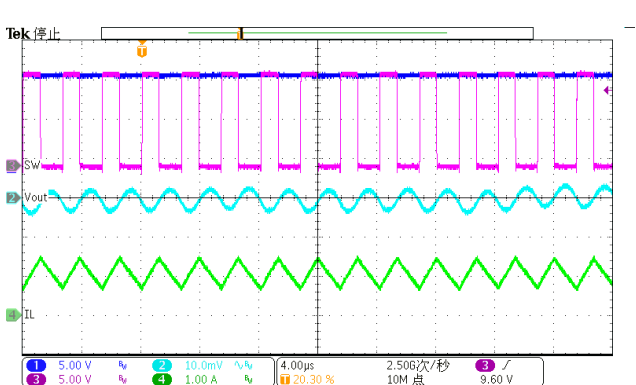
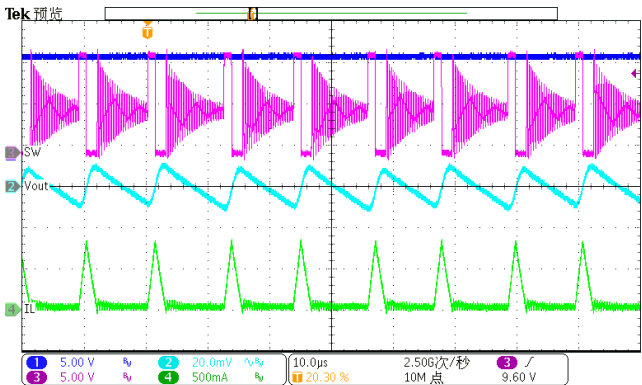
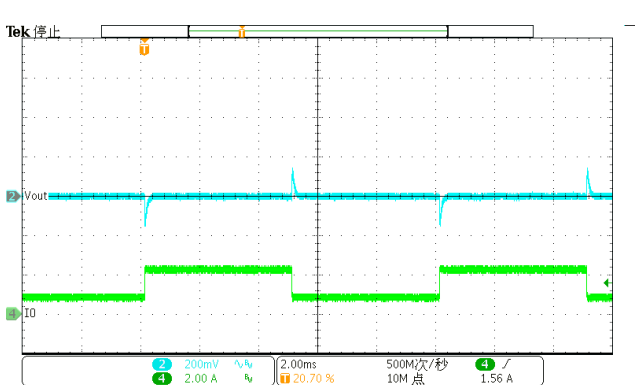
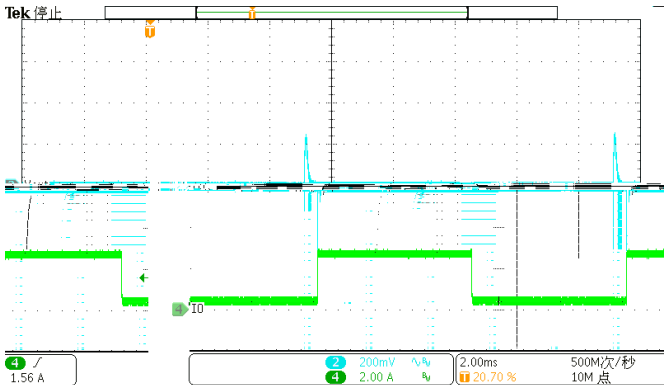
Table 4: Typical External Component Values

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Application Waveforms



Application Waveforms



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TAPE AND REEL INFORMATION

